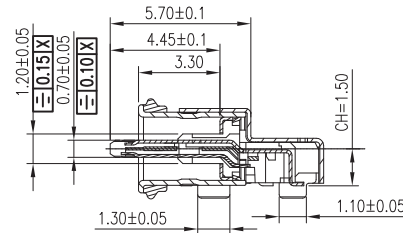
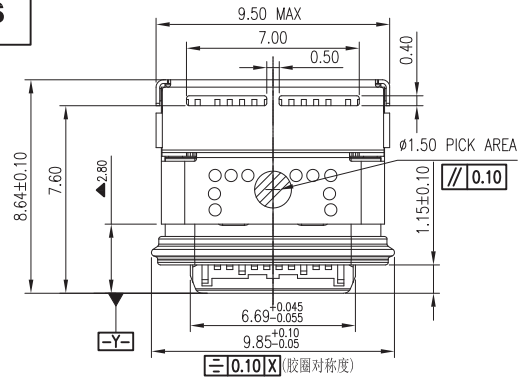


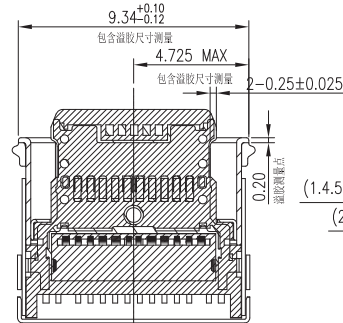
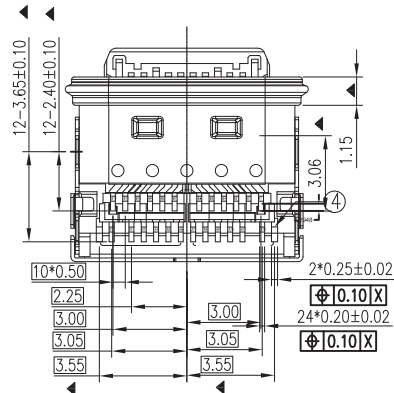
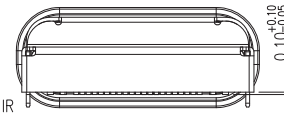
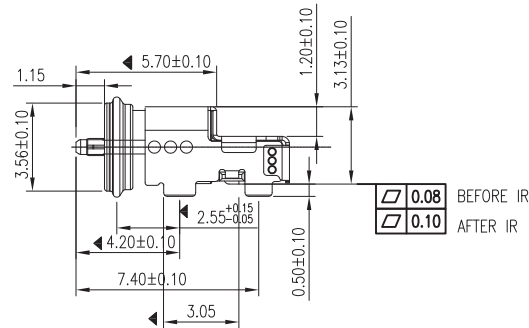
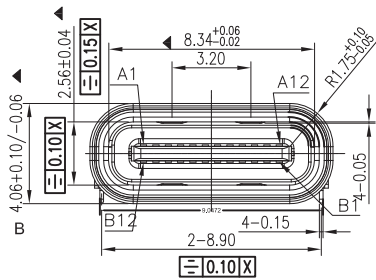
RoHS



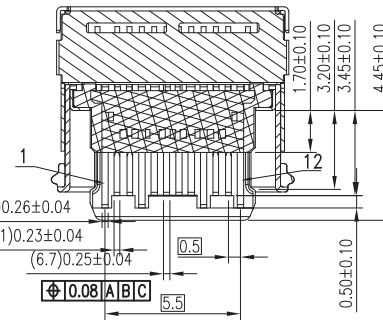
SECTION C-C(SC 1:1)



1. ELECTRICAL CHARACTERISTICS:
- 1.1 CONTACT RESISTANCE: 40mΩ Max FOR INITIAL. 10mΩ CHANGE AFTER TEST. MEASURE AT 20mV, 100mA.
 - 1.2 CONTACT CURRENT RATING: 5A FOR COLLECTIVELY POWER SUPPLY PIN(PIN1, A4, A9, A12, B1, B4, B9, B12); 1.25A FOR VCONN PIN; 0.25A FOR OTHER SIGNAL PIN.
 - 1.3 DIELECTRIC WITHSTANDING VOLTAGE: 100V AC R.M.S.
 - 1.4 INSULATION RESISTANCE 100MΩ Min
 - 1.5 OPERATING TEMPERATURE: -40° C ~ 85° C
2. MECHANICAL CHARACTERISTICS:
- 2.1 MATING FORCE: 5~20 N.
 - 2.2 UNMATED FORCE: 8~20N INITIAL TEST; 6~20N DURABILITY AFTER TEST
 - 2.3 DURABILITY: 10,000 CYCLES



SECTION A-A(SC 1:1)



SECTION B-B(SC 1:1)

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTxp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	Vbus	Bus Power	B9	Vbus	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuration Channel
A9	Vbus	Bus Power	B4	Vbus	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

GENERAL TOLERANCE
.X ±0.30
.XX ±0.20
.XXX±0.10

PROJECT
① ②



瑞澤電子股份有限公司
Horustech Electronics Co., Ltd

PART NO. HTCC470004

TITLE.
TYPE C/F 24PIN 防水IP68板上双贴 SMT

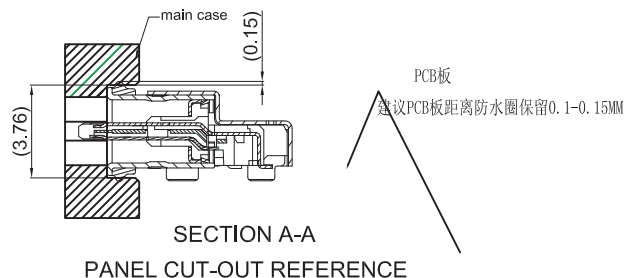
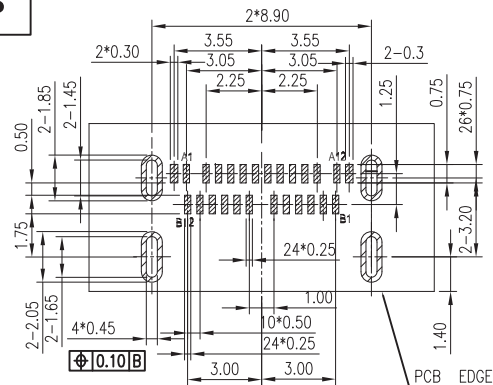
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APPROVAL	CHECK	DRAWN	DATA
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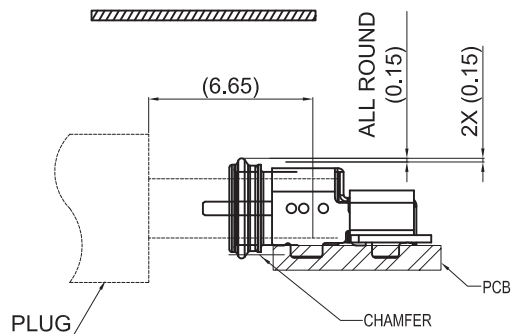
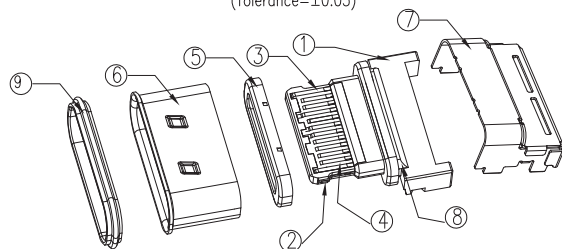
Frank	Jason	Sam	2021-06-18
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UNIT	SCALE	NUMBER	REV
MM	1:1	A-00	A

RoHS

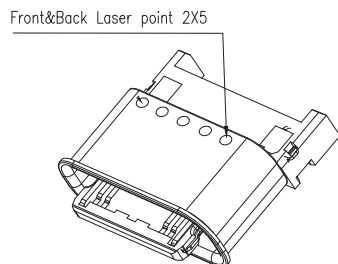
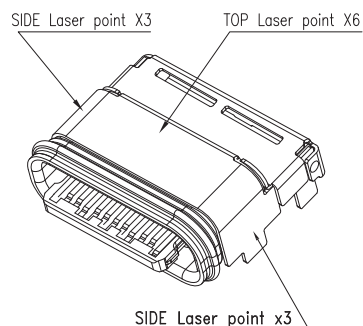


RECOMMENDED PCB LAYOUT
(Tolerance=±0.05)



说明:

1. Colplanarity: 0.08最大红外线前。 , 0.10mm MAX
2. 舌PIN的位置:IR前 : 0.08mm MAX; IR 后0.12mm MAX;
3. P/N:
4. 包装方式 : 载带包装
5. 接触区和焊接区无毛刺, 除特说明, 冲压毛刺为0.05mm MAX.
6. 未标注尺寸公差为自由公差为准.
7. 成品端子平面度必须100%检测.
8. 成品可靠性测试要求参照规格书.
9. 产品满足 ROHS 2.0



9	密封圈	1PCS	硅胶, 黑色	
8	胶水	1PCS	环氧胶, 黑色	
7	外壳	1PCS	SUS304 , T=0.15. 镀镍50u" MIN.	
6	内拉伸壳	1PCS	SUS304, T=0.20.	
5	EMI拉伸壳	1PCS	SUS304 , T=0.12. 镀镍50u" MIN.	
4	上排端子	1SET	C18400, T=0.12. 底层镀镍 50-100u", 功能接触区单面	
3	下排端子	1SET	刷镀 Au 15u", 焊接区双面镀金 G/F.	
2	中板	1PCS	SUS301-1/4 H, 镀镍50u" MIN.	
1	塑胶	1PCS	PA46 BLACK	
项目	组成部件	用量	材质/处理	备注

GENERAL
TOLERANCE
.X ±0.30
.XX ±0.20
.XXX±0.10
PROJECT



瑞澤電子股份有限公司
Horustech Electronics Co., Ltd

PART NO. HTCC470004

TITLE.
TYPE C/F 24PIN 防水IP68板上双贴 SMT

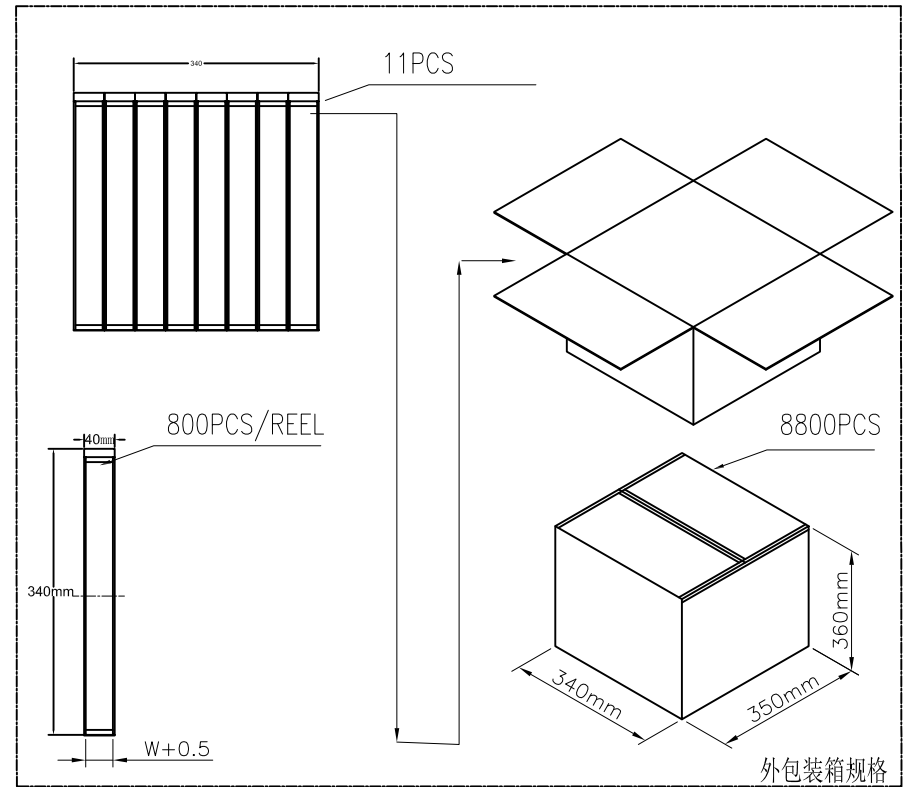
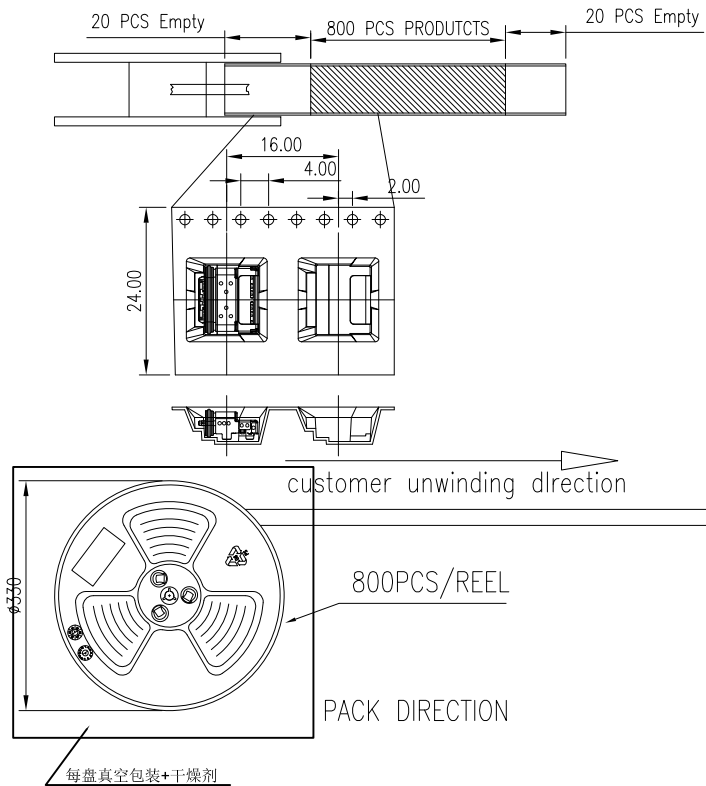
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PROJECT	FRANK
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APPROVAL	CHECK	DRAWN	DATA
Frank	Jason	Sam	2021-06-18

UNIT	SCALE	NUMBER	REV
MM	1 : 1	A-00	A

RoHS



	REEL	CARTON
CONNECTOR (PCS)	800	8800

							GENERAL TOLERANCE . $\times$ ± 0.30 . $\times \times$ ± 0.20 . $\times \times \times$ ± 0.10	 瑞澤電子股份有限公司 Horustech Electronics Co., Ltd	PART NO. HTCC470004						
									TITLE. TYPE C/F 24PIN 防水IP68板上双贴 SMT						
									APPROVAL	CHECK	DRAWN	DATA			
DATE	OLD REV	NEW REV	ECN	DESCRIPTION	CHECK	APPROVAL	PROJECT 	Frank	Jason	Sam	2021-06-18	UNIT MM	SCALE 1 : 1	NUMBER A-00	REV A